

ABSTRACT OF THE DISCLOSURE

1 In a speed converter for an IEEE-1394 serial bus network, first and
2 second communication device nodes are attached to first and second
3 buses. A first transceiver node receives an inbound first packet at a first
4 speed from the first bus and transmits an inbound second packet, which
5 is received from the second bus by a second transceiver node at a second
6 speed, as an outbound second packet at the first speed to the first bus.
7 The second transceiver node transmits the inbound first packet as an
8 outbound first packet at a second speed to the second bus. Header
9 translation circuitry translates the destination identifier of the inbound
10 first packet to the destination identifier of the outbound first packet
11 according to a mapped relationship between the first transceiver node
12 and the second communication node, and translates the destination
13 identifier of the inbound second packet to the destination identifier of
14 the outbound second packet.

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